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19. (original) The apparatus of claim 18, wherein the fixture impedance comprises a capacitor coupled between the common I/O and a fixed potential relative to the semiconductor device.

20. (new) The apparatus of claim 14, wherein the weak driver impedance includes a variable impedance.

## **REMARKS**

Currently pending claims 1-20 are for consideration by the Examiner. Prompt and favorable examination on the merits is respectfully requested.

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